

PATENT ABSTRACTS OF JAPAN

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(71)Applicant : MATSUSHITA ELECTRIC IND CO LTD

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(54) PACKET COMPOSING DEVICE

(57)Abstract:

PURPOSE: To reduce the dispersion in delay times by reading a data from a buffer memory when a data quantity counter counts a prescribed data quantity or a timer circuit measures a prescribed time, and generating a packet and sending it.

CONSTITUTION: A timer circuit 16 receives a data residual amount signal 19 from a buffer memory 13 to start the measurement of a data resident time in the buffer memory. When a data quantity in the buffer memory does not reach one packet through the arrival of a new transmission data till a preset prescribed time T after the transmission of an output packet 212, the timer circuit 16 sends a command signal 21 commanding the transmission of unsent packet to a packet transmission circuit 15 after the time T. In this case, a data 18 read from the buffer memory 13 does not meet the data quantity by one packet, then the packet transmission circuit 15 adds a dummy data 223 to the data 222 by final one unit data of the transmission data 205 and sends the result as an output packet 213. Thus, the resident time of the transmission data is kept within a prescribed time.

